

REMARKS

An Office Action was mailed December 28, 2010. This response is timely. Any fee due with this paper, including any necessary extension fees, may be charged on Deposit Account 50-1290.

Summary

Claims 1, 3, 11, 13, 21, and 25-28 are being examined. Claims 1, 11, 21, and 28 are the independent claims that are being examined.

By the foregoing, claims 1, 11, 21, and 28 are amended. No new matter has been added.

Rejection under 35 U.S.C. §102(b) and 35 U.S.C. §103(a)

Claims 1, 3, 11, 13, and 28 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 6,232,945 to Moriyama. Claims 25-27 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Moriyama in view of U.S. Patent No. 6,734,840 to Fukutofu. Independent claim 21 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Moriyama in view of JP 2001-249643 to Hirobumi. Thus, all claims stand rejected at least over Moriyama.

The Response to Arguments notes that the claims remain broad and undefined. *"Due to the lack of language which describes how the polarity correlates to the data lines, the teachings of Moriyama continue to read upon the claim language as stated."* OA at 2. The rejection avers that Moriyama teaches that the signal has a certain polarity as taught by Fig. 3 and includes data lines as taught by Fig. 1, items X1-Xm.

The presently claimed invention includes the follow features:

- (a) The polarity of the data voltage is inverted in every set of two or more successive horizontal synchronizing periods, and the data voltage is reset in a blanking period of each of the successive horizontal synchronizing periods of the set.

- (b) The resetting operation is performed with reference to a latch signal that is started between the end of the writing period and the end of the blanking period.
- (c) The resetting operation is completed before the writing period when the polarity of the data voltage is not inverted.
- (d) The polarity of the data voltage is not changed, i.e., inverted before and after the resetting operation.

These features are readily found in Fig. 7 and the accompanying description. Specifically, as disclosed in Fig. 7 of the present invention, the data voltage (i.e., the drain voltage in Fig. 7) is reset at the time t3 by the latch signal. As a result, the value of the data voltage at the time t4 (i.e., the start of the second horizontal synchronizing period of the set) is equalized to the value at the time t1 (i.e., the start of the first horizontal synchronizing period of the set). The polarity of the data voltage is not changed or inverted before and after the resetting operation.

In this way, due to this resetting operation, the waveform of the data voltage in the second horizontal synchronized period (from t4 to t7) is equalized to the waveform of the data voltage in the first horizontal synchronizing period (from t1 to t4).

The presently claimed invention defines the polarity as *"being the same before and after the resetting operation."* Support thereof can be found in Fig. 7 and the accompanying description. No new matter has been added.

In contrast, Moriyama teaches in Fig. 3 that the polarity of the video signal, which corresponds to the data voltage of the present invention, is inverted by the reset signal between the two horizontal synchronizing periods. Thus, Moriyama fails to teach, disclose, or suggest polarity that is the same before and after the resetting operation.

Accordingly, the Examiner is respectfully requested to withdraw the rejections.

In view of the remarks set forth above, this application is believed to be in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,

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